AIMB-258

Intel[®] Core[™]2 Duo Mini-ITX with VGA/DVI/LVDS, 6 COM,and Dual LAN



Features

- Supports Intel[®] Core[™]2 Duo mobile processor uFC-PGA 478 dual channel DDR3 800/1066 MHz SDRAM and max. 4 GB dual channel DDR3 800/1066 MHz SDRAM
- Intel GM45 and ICH9M supports FSB 667/800/1066 MHz
- Supports dual display for VGA, LVDS, and DVI
- PCIe x16 expansion for add-on cards
- Supports Embedded Software APIs and Utilities

Software APIs:	SMBus H/W Monitor GPI0	Watchdog Brightness
Utilities:	BIOS flash Monitoring	

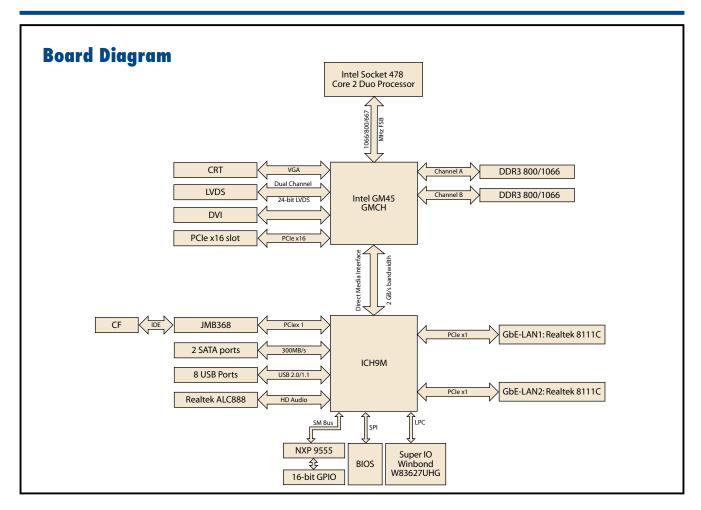
Windows Embeddec

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Specifications

	CPU (45 nm µFC-PGA 478)	Intel Core 2 Duo	Intel Celeron M		
	Max. Speed	T9400 2.53 GHz	575 2.0 GHz		
Propagar System	Front Side Bus	667/800/1066 MHz	667		
Processor System	L2 Cache	6 MB	1 MB		
	Chipset	GM45 + ICH9M			
	BIOS	Award 16 Mbit, SPI			
	PCI	-			
xpansion Slot	Mini-PCI	-			
	PCle x16	4 GB/s per direction, 1 slot (if F	PCle x16 is used, DVI is automatica	lly disabled)	
	Technology	DDR3 800/1066 MHz SDRAM			
/lemory	Max. Capacity	4 GB			
loniory	Socket	2 x 204-pin SODIMM			
	Controller		raphics Media Accelerator X4500		
	VRAM	Shared system memory up to 3	84 MB video memory		
	LVDS	Single channel 18/24-bit/Dual			
raphics	TV-Out	Single channel 10/24-bit/Dual			
	DVI	Yes (if DVI is used, PClex 16 is	automatically disabled)		
	Dual Display	CRT + DVI; CRT + LVDS; DVI +			
	Interface	10/100/1000 Mbps	LVDS		
thorpot			ChE LANO: Dealtak DTI 01110		
Ethernet Controller GbE LAN1: Realtek RTL8111C; GbE LAN2: Realtek RTL8111C					
	Connector	RJ-45 x 2			
ATA	Max Data Transfer Rate	300 MB/s			
	Channel	2			
SD	CompactFlash	Supports CompactFlash Type I,	11		
	VGA	1			
	DVI	1			
	Ethernet	2			
ear I/O	USB	4 (USB 2.0 compliant)			
	Audio	3 (Mic-in, Line-in, Line-out)			
	Serial	2 (1 of RS-232, 1 of RS-232/42	22/485)		
	PS/2	2 (1 x keyboard and 1 x mouse			
	LVDS	1			
	USB	4 (USB 2.0 compliant)			
	Serial	4 (RS-232)			
	IDE	-			
	SATA	2			
Internal Connector	CompactFlash	1			
	Parallel	-			
	IrDA	_			
	FDD	-			
	DIO	16-bit General Purpose I/O for			
Watchdog Timer	Output	System reset			
	Interval	Programmable 1 ~ 255 sec/mir			
•				E Vab	-12 V
ower Requirements	Power On		12 V	5 Vsb	
		0.99 A 2.67 A	2.07 A	0.17 A	0.08 A
nvironment	Transaction	Operating	Non-Operating	4500 E)	
	Temperature	<u>0~60°C (32~140°F)</u>	-20 ~ 70° C (-4 ~	158° F)	
hysical Characteristics	Dimensions	170 mm x 170 mm (6.69" x 6.6	9")		

All product specifications are subject to change without notice



Ordering Information

	Display	GbE	SATA	Serial	CF
AIMB-258G2-00A1E	VGA/DVI/ LVDS	Dual	2	6	1

Packing List

Part number	Description	Quantity
1700003194	SATA HDD cable	2
1700017461	SATA power cable	2
1750000348	CPU cooler	1
1960019193T100	I/O port bracket	1
2006025810	Startup manual	1
2066025800	Driver CD	1
1701400181	Cable kits for 4 serial ports	1

Optional Accessories

Part Number	Description
1700003195	USB cable with four ports, 17.5 cm
1700002204	USB cable with four ports, 27 cm
1700008461	USB cable with four ports, 30.5 cm

Embedded OS

OS	Part No.	Description
	2070009518	Image WES2009 AIMB-258 V4.0 ENG
Win XPE	2070009657	XPE WES2009 AIMB-258 V4.0 CHT
	2070009658	XPE WES2009 AIMB-258 V4.0 MUI24

Bracket View



AIMB-258G2-00A1E

Value-Added Software Services

Software API: An interface that defines the ways by which an application program may request services from libraries and/or operating systems. Provides not only the underlying drivers required but also a rich set of user-friendly, intelligent and integrated interfaces, which speeds development, enhances security and offers add-on value for Advantech platforms. It plays the role of catalyst between developer and solution, and makes Advantech embedded platforms easier and simpler to adopt and operate with customer applications.

Software APIs

Control



General Purpose Input/Output is a flexible parallel interface that allows a variety of custom connections. It allows users to monitor the level of signal input or set the output status to switch on/off a device. Our API also provides Programmable GPIO, which allows developers to dynamically set the GPIO input or output status.



SMBus is the System Management Bus defined by Intel® Corporation in 1995. It is used in personal computers and servers for low-speed system management communications. The SMBus API allows a developer to interface a embedded system environment and transfer serial messages using the SMBus protocols, allowing multiple simultaneous device control.



I²C is a bi-directional two wire bus that was developed by Philips for use in their televisions in the 1980s. The I²C API allows a developer to interface with an embedded system environment and transfer serial messages using the I²C protocols, allowing multiple simultaneous device control.

Display



Control

The Brightness Control API allows a developer to interface with an embedded device to easily control brightness.



The Backlight API allows a developer to control the backlight (screen) on/off in an embedded device.

Backlight

Software Utilities



The BIOS Flash utility allows customers to update the flash ROM BIOS version, or use it to back up current BIOS by copying it from the flash chip to a file on customers' disk. The BIOS Flash utility also provides a command line version and API for fast implementation into customized applications.



The embedded application is the most important property of a system integrator. It contains valuable intellectual property, design knowledge and innovation, but it is easily copied! The Embedded Security ID utility provides reliable security functions for customers to secure their application data within embedded BIOS.



The Monitoring utility allows the customer to monitor system health, including voltage, CPU and system temperature and fan speed. These items are important to a device; if critical errors happen and are not solved immediately, permanent damage may be caused.

Monitor



A watchdog timer (WDT) is a device that performs a specific operation after a certain period of time if something goes wrong and the system does not recover on its own. A watchdog timer can be programmed to perform a warm boot (restarting the system) after a certain number of seconds.



The Hardware Monitor (HWM) API is a system health supervision API that inspects certain condition indexes, such as fan speed, temperature and voltage.



The Hardware Control API allows developers to set the PWM (Pulse Width Modulation) value to adjust fan speed or other devices; it can also be used to adjust the LCD brightness.

Power Saving



Make use of Intel SpeedStep technology to reduce power power consumption. The system will automatically adjust the CPU Speed depending on system loading.



Throttling

Refers to a series of methods for reducing power consumption in computers by lowering the clock frequency. These APIs allow the user to lower the clock from 87.5% to 12.5%.



The eSOS is a small OS stored in BIOS ROM. It will boot up in case of a main OS crash. It will diagnose the hardware status, and then send an e-mail to a designated administrator. The eSOS also provides remote connection: Telnet server and FTP server, allowing the administrator to rescue the system.



Flash Lock is a mechanism that binds the board and CF card (SQFlash) together. The user can "Lock" SQFlash via the Flash Lock function and "Unlock" it via BIOS while booting. A locked SQFlash cannot be read by any card reader or boot from other platforms without a BIOS with the "Unlock" feature.